

What is claimed is:

1. A simultaneous broadcasting receiver comprising:

a first receiver for receiving a first broadcasting signal broadcast in a first broadcasting system and applying predetermined signal processing to the received first broadcasting signal;

a second receiver for receiving a second broadcasting signal transmitted by using a second broadcasting system different from the first broadcasting system and transmitting a broadcasting content in synchronization with an identical broadcasting content transmitted by the first broadcasting signal, the second receiver for applying predetermined signal processing to the received second broadcasting signal;

a reception status detector for detecting a reception status of the first broadcasting signal;

an input system selector for selecting between a processed signal obtained in the first receiver and a processed signal obtained in the second receiver and outputting the selected signal to a circuit following thereto;

a power supply controller for controlling supplying power to the second receiver; and

a controller for controlling the input system selector and the power supply controller according to a detection result obtained by the reception status detector.

2. A simultaneous broadcasting receiver as claimed in claim 1,

wherein the controller controls the input system selector and the power supply controller so that,

a) when the reception status of the first broadcasting signal is better than a first status, power is not supplied to the second receiver and the processed signal obtained in the first receiver is selected and outputted;

b) when the reception status of the first broadcasting signal is poorer than the first status and better than a second status, power is supplied to the second receiver and the processed signal obtained in the first receiver is selected and outputted; and

c) when the reception status of the first broadcasting signal is poorer than the second status, power is supplied to the second receiver and the processed signal obtained in the second receiver is selected and outputted.

3. A simultaneous broadcasting receiver as claimed in claim 2,
wherein the first broadcasting system is digital and the second broadcasting system is analog, and

wherein the reception status detector detects the reception status of the first broadcasting signal based on a BER (Bit Error Rate) of a demodulated signal obtained in the first receiver.

4. A simultaneous broadcasting receiver as claimed in claim 3,
wherein the first receiver includes:
a tuner for extracting a target frequency band signal from the received first broadcasting signal and performing a frequency conversion by converting the extracted target frequency band signal to an intermediate frequency signal; and

an OFDM (Orthogonal Frequency Division Multiplex) demodulator for decoding the intermediate frequency signal obtained in the tuner into a decoded signal in digital form,

wherein the reception status detector detects the reception status of the first broadcasting signal based on the BER of the decoded signal obtained in the OFDM demodulator.

5. A simultaneous broadcasting receiver as claimed in claim 1 further comprising, a memory portion, arranged in a stage immediately before the input system selector, for memorizing temporarily each of the processed signals obtained in the first and the second receivers respectively.

6. A simultaneous broadcasting receiver comprising:

- a first receiver for receiving a first broadcasting signal broadcast in a first broadcasting system and applying predetermined signal processing to the received first broadcasting signal;
- a second receiver for receiving a second broadcasting signal transmitted by using a second broadcasting system different from the first broadcasting system and transmitting a broadcasting content in synchronization with an identical broadcasting content transmitted by the first broadcasting signal, the second receiver for applying predetermined signal processing to the received second broadcasting signal;
- a reception status detector for detecting a reception status of the first broadcasting signal;
- an input system selector for selecting between a processed signal obtained in the first receiver and a processed signal obtained in the second receiver and outputting the selected signal to a circuit following thereto;
- a memory portion, arranged in a stage immediately before the input system selector, for memorizing temporarily each of the processed signals obtained in the first and the second receivers respectively; and
- a controller for controlling the input system selector and the memory portion according to a detection result obtained by the reception status detector.

7. A simultaneous broadcasting receiver as claimed in claim 6,
wherein the controller controls the input system selector and the memory portion so that,
a) when the reception status of the first broadcasting signal is better than a predetermined status, the processed signal obtained in the first receiver is selected and outputted; and
b) when the reception status of the first broadcasting signal is poorer than the predetermined status, the processed signal obtained in the second receiver is selected and outputted.

8. A simultaneous broadcasting receiver as claimed in claim 7,
wherein the first broadcasting system is digital and the second broadcasting system is analog, and
wherein the reception status detector detects the reception status of the first broadcasting signal based on a BER (Bit Error Rate) of the decoded signal obtained in the first receiver.

9. A simultaneous broadcasting receiver as claimed in claim 8,
wherein the first receiver includes
a tuner for extracting a target frequency band signal from the received first broadcasting signal and performing a frequency conversion by converting the extracted target frequency band signal to an intermediate frequency signal, and
an OFDM (Orthogonal Frequency Division Multiplex) demodulator for decoding the intermediate frequency signal obtained in the tuner into a decoded signal in digital form,

wherein the reception status detector detects the reception status of the first broadcasting signal based on the BER of the decoded signal obtained in the OFDM demodulator.